



D2.6 FPGA Implementation and verification in camera



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Table of Contents

1. EXECUTIVE SUMMARY	4
2. INTRODUCTION	5
2.1. BACKGROUND	5
2.2. Camera Architecture	5
2.3. Project Status	6
3. ELECTRONIC VIEWFINDER	7
3.1. Overview	7
3.2. Display	7
3.3. Interface	8
4. FPGA IMAGE PROCESSING	10
4.1. Overview	10
4.2. Signal Path	10
4.3. 3DLUT Transformation	10
4.4. Matrix Transformation	12
4.5. LUT Transformation	12
4.6. Histogram	12
5. SOFTWARE	13
5.1. Overview	13
5.2. Generation of FPGA Parameters	13
5.3. Challenges	14
6. LITERATURE	15

1. EXECUTIVE SUMMARY

This document describes the implementation of elements of the framework described in *D2.5 Real-time HDR to SDR conversion in a generic viewing environment*. The implementation is for a new camera system that was planned for release in early 2020.

In this camera system, images are simultaneously transformed for SDR, HDR, and the electronic viewfinder, which has a lower peak luminance than the HDR reference monitor.

The implementation uses multiple 3DLUT transformations of the LogC image.

Most modules of the implementation are ready, but tests with the integrated system have not been possible because the project is delayed.